

Low Cost, 275MHz Rail-to Rail Amplifiers

Advanced Data

FEATURES

Low Cost Single (AD8061) Dual (AD8062) Single with Disable (AD8063) Quad (AD8064) Rail-to-rail Output swing **High Speed** 275MHz, -3 dB Bandwidth (G = +1) 900V/ms Slew Rate Operates on 2.7V to 12V Supplies Excellent Video Specs (RL = 150Ω , G = +2) Gain Flatness 0.1 dB to 30MHz 0.01% Differential Gain Error 0.05° Differential Phase Error Low Power 7.2mA/Amplifier Typ Supply Current AD8063 300µA when disabled Small Packaging AD8061 Available in SOIC-8 and SOT23-5 AD8062 Available in SOIC-8 and μ SOIC AD8063 Available in SOIC-8 AD8064 Available in SOIC-14 and TSSOP

APPLICATIONS

Imaging Photodiode Pre-amp Professional Cameras Hand Sets DVD / CD Filters A-to-D Driver

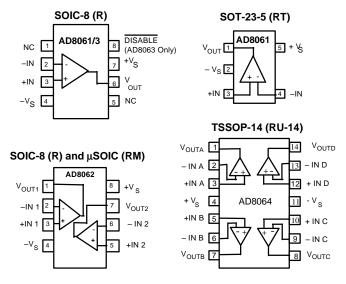
PRODUCT DESCRIPTION

The AD8061, AD8062, AD8063, and AD8064 are rail-torail out voltage feedback amplifiers offering ease of use and low cost. They have bandwidth and slew rate typically found in current feedback amplifiers. All have a wide input voltage range and output voltage swing making them easy to use on single supplies as low as 2.7V.

Despite being low cost, the AD8061, AD8062, AD8063, and AD8064 provide excellent overall performance. For video applications their differential gain and phase errors are 0.01% and 0.052° into a 150Ω load, along with 0.1dB flatness out to 30MHz. Additionally, they offer wide bandwidth to

AD8061/62/63/64

CONNECTION DIAGRAMS (TOP VIEW)



275MHz along with 900V/ μ s slew rate.

The AD8061, AD8062, AD8063, and AD8064 offer low power of 7.2mA/amplifier, while being capable of delivering up to 85mA of load current. The AD8063 has a power down disable feature that reduces the supply current to 300μ A. These features make the AD8063 ideal for portable and battery powered applications where size and power is critical.

| Model | Operating Temperature R | ange Package |
|-----------|----------------------------|----------------|
| AD8061AR | -40 to +85°C | 8 Lead SOIC |
| AD8061ART | -40 to +85°C | 5 Lead SOT23-5 |
| AD8062AR | -40 to +85°C | 8 Lead SOIC |
| AD8062ARM | -40 to +85°C | 8 Lead µSOIC |
| AD8063AR | -40 to +85°C | 8 Lead SOIC |
| AD8064AR | -40 to +85°C | 14 Lead SOIC |
| AD8064ARU | -40 to +85°C | 14 Lead TSSOP |

This information applies to a product under development. Its characteristics and specifications are subject to change without notice. Analog Devices assumes no obligation regarding 1 future manufacturing unless otherwise agreed to in writing. Rev A 5/21/98

| | | AD8061/62/63/64 | |
|--|--|--------------------------------|----------|
| Parameter | Conditions | Min Typ Max | Units |
| DYNAMIC PERFORMANCE | | | |
| -3 dB Bandwidth | $G = +1, V_0 = 0.2Vp-p$ | 275 | MHz |
| | $G = -1, +2, V_0 = 0.2Vp-p$ | 115 | MHz |
| | $G = +1, V_0 = 2Vp-p$ | TBD | MHz |
| Bandwidth for 0.1 dB Flatness | $V_{0} = 0.2Vp-p,$ | 30 | MHz |
| Slew Rate | $G = +1$, $V_0 = 2V$ Step , $R_1 = 2k\Omega$ | 750 | V/µs |
| | | 900 | V/μs |
| 0 at the prime to 0 10/ | $G = +1$, $V_0 = 4V$ Step , $R_L = 2k\Omega$ | | |
| Settling Time to 0.1% | $G = +2, V_o = 2V$ Step | 14 | ns |
| NOISE/HARMONIC PERFORMANCE | | | |
| Total Harmonic Distortion | $f_C = 5 \text{ MHz}, V_o = 2V \text{ p-p}, R_L = 1k\Omega$ | -80 | dBc |
| | $f_C = 20 \text{ MHz}, V_o = 2V \text{ p-p}, R_L = 1k\Omega$ | -50 | dBc |
| Crosstalk, Output to Output | f = 5 MHz, G = +2 | -60 | dB |
| Input Voltage Noise | f = 100 kHz | 8.5 | nV/√Hz |
| Input Current Noise | f = 100 kHz | 1 | pA/√Hz |
| Differential Gain Error | NTSC, G = +2, $R_L = 150 \Omega$ | 0.01 | % |
| Differential Phase Error | NTSC, $G = +2$, $R_L = 150 \Omega$ | 0.05 | Degree |
| Third Order Intercept | f = 10 MHz | TBD | dBM |
| SFDR | F = 5 MHz | TBD | dB |
| DC PERFORMANCE | | | ub |
| | | 1 6 | mV |
| Input Offset Voltage | т. т. | | mV mV |
| | T _{min} - T _{max} | TBD | mV |
| Input Offset Voltage Drift | | 3.5 | µV/⁰C |
| Input Bias Current | | 0.1 1 | μA |
| | T _{min} - T _{max} | TBD | μA |
| Input Offset Current | | 0.3 | ±μA |
| Open Loop Gain | $V_o = \pm 2.5 \text{ V}, R_L = 150\Omega$ | 88 | dB |
| | $V_o = \pm 2.5 \text{ V}, \text{ R}_L = 2k\Omega$ | TBD | dB |
| INPUT CHARACTERISTICS | | | |
| Input Resistance | | 10 | MΩ |
| Input Capacitance | +Input | 2 | pF |
| Input Common-Mode Voltage Range | $R_{1} = 1k\Omega$ | -5.3 to 3.2 | V |
| Common-Mode Rejection Ratio | $V_{CM}^{L} = \pm 2.5 V$ | 80 | dB |
| OUTPUT CHARACTERISTICS | | | |
| | $P_{-} = 150 O_{-}$ | 4.5 to -4.5 | v |
| Output Voltage Swing | $R_L = 150 \Omega$ | | |
| | $R_{L} = 2k\Omega$ | 4.9 to -4.9 | V |
| Output Current | $V_{o} = +/-4 V$ | 85 | mA |
| Capacitive Load Drive | 30% over shoot | 520 | pF |
| POWER DOWN DISABLE | | | |
| Turn-on Time | | TBD | ns |
| Turn-off Time | | TBD | ns |
| Input Voltage - Disabled | | TBD | V |
| Input Voltage - Enabled | | TBD | V |
| POWER SUPPLY | | | |
| | | 2.7 10 12 | v |
| | | | mA |
| | | | mA |
| | | | dB |
| Operating Range Quiescent Current per Amplifier Supply Current when Disabled Power Supply Rejection Ratio | | 2.7 10 12 7.2 0.3 -80 | m. m. |

SPECIFICATIONS (@T_A = +25°C, V_S = +/-5V, R_L = 100 Ω , R_F = 0 Ω , Gain =+1, unless otherwise noted)

This information applies to a product under development. Its characteristics and specifications are subject to change without notice. Analog Devices assumes no obligation 2 regarding future manufacturing unless otherwise agreed to in writing. Rev A 5/21/98